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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/835,249	04/13/2001	Yogendra K. Chawla	C34932/118975	5026

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EXAMINER

NGUYEN, KHANH V

ART UNIT

PAPER NUMBER

2817

DATE MAILED: 07/31/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	09/835,249	CHAWLA ET AL.
	Examiner	Art Unit
	Khanh V. Nguyen	2817

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 09 May 2002.

2a) This action is FINAL. 2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1-16 and 19-46 is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5) Claim(s) _____ is/are allowed.

6) Claim(s) 1-16, 19-46 is/are rejected.

7) Claim(s) _____ is/are objected to.

8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

11) The proposed drawing correction filed on _____ is: a) approved b) disapproved by the Examiner.
 If approved, corrected drawings are required in reply to this Office action.

12) The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
 a) The translation of the foreign language provisional application has been received.

15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413) Paper No(s). _____.
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) Notice of Informal Patent Application (PTO-152)
 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____. 6) Other: _____

DETAILED ACTION

Claim Rejections - 35 USC § 112

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 11, 14, 15, 32, 33, 45, 46 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

It is not cleared which elements from the disclosed Figures constitute first and second low pass filter claimed.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-6, 9, 10, 12, 13, 16, 21-27, 29-31, 34-40, 42-44 are rejected under 35 U.S.C. 102(b) as being anticipated by Chawla et al. (6,046,641).

Regarding claims 1, 21, 34, Chawla et al. (Figs. 3 and 6) disclose an amplifier circuit comprising: a field effect transistor (Q1) read as a semiconductor devices having a control terminal (G) and two conducting terminals (D and S) wherein the first and second conducting terminals coupled to a ground potential and the output (RFout) is coupled to the second conducting terminal (S); an RF source (RFin) coupled to control

terminal (G) of semiconductor devices (Q1); inductor-capacitor combination (L1-C6) can be read as a resonant inductor circuit coupled to the second conducting terminal; and elements (L4, R9, C11) can be read as a filter coupled to the second conducting terminal.

Regarding claims 2-4, 22-24, 35-37, wherein the RFin is capable of receiving different signals having different waveforms and the type of wave used is an intended use of the invention.

Regarding claims 5, 6, 26, 27, 39, 40, Chawla wherein semiconductor device is a field effect transistors (Q1). FET and MOSFET are considered the same.

Regarding claims 9, 29, 42, Chawla et al. (Fig. 6) disclose plurality of FETs (Q2A-Q2D) connected in parallel and sharing an RFin, wherein FETs can be read as a plurality of discrete transistor.

Regarding claims 10, 30, 43, et al. disclose power transistors are arranged in a KPT configuration. (see col. 2, lines 60-62)

Regarding claims 12, 13, 25, 31, 38, 44, wherein resonant inductor circuit comprises inductor (L1) coupled to voltage supply (Vs) which can be a DC voltage source and the function would be inherent.

Regarding claim 16, Chawla et al. utilized voltage standing wave ratio (VSWR). (see col. 2, lines 52-54).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 7, 8, 11, 14, 15, 19, 20, 28, 32, 33, 41, 45, 46 are rejected under 35 U.S.C. 103(a) as being unpatentable over Chawla et al.

Regarding claims 7, 28, 41, it is known in the art that field effect transistor and bipolar transistor can be used interchangeably.

Regarding claim 8, the type of transistor used is considered a matter of design choice in the absence of unexpected result.

Regarding claims 11, 14, 15, 32, 33, 45, 46, Chawla et al. disclose the claimed invention except a second filter having connection as claimed. However, it would have been obvious to one having ordinary skill in the art at the time the invention was made to provide an additional filter since an additional filter would only enhance the circuit ie. filter out noise or filter out unwanted signals etc.

Regarding claims 19, 20, the output of a RF signal source is either fixed or varies depend on the circuit design and the intended use of the invention.

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Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. The additional reference (Samay et al. (5,276,406); Adishian et al. (6,157,258)) shows further analogous prior art circuitry (having FET).

This art is deemed relevant and should be carefully reviews before any amendment is filed.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Khanh V. Nguyen whose telephone number is (703) 306-9058. The examiner can normally be reached from 8:00 AM-5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on (703) 308-4909. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9318 for regular communications and (703) 872-9319 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Customer Service at (703) 872-9317.

NKV

07/25/02



Nguyen, Khanh Van

Group 2800, Art Unit 2817